

NOV 7 - 2006

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To:	Examiner Guy J. Lamarre	From:	Timothy N. TROP
Company:	US Patent Office	Date:	November 7, 2006
Fax:	(571) 273-8300	Pages:	9 (including cover sheet)
Your Re:	10/687,124	Our Re:	ITL1046US

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MESSAGE:

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	Richard E. Fackenthal	§	Art Unit:	2133
Serial No.:	10/687,124	§	Examiner:	Guy J. Lamarre
Filed:	October 16, 2003	§	Docket:	ITL.1046US P17448
For:	Error Correction for Multi- Level Cell Memory with Overwrite Capability	§	Assignee:	Intel Corporation

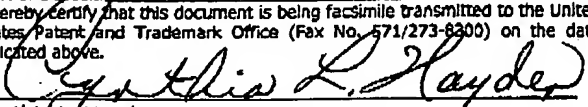
Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed October 30, 2006, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

Date of Deposit: November 7, 2006
I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 571/273-8300) on the date indicated above.

Cynthia L. Hayden